

IN THE DRAWINGS

Attached are formal drawings for the present application.

Attachment: formal drawings (14 sheets)

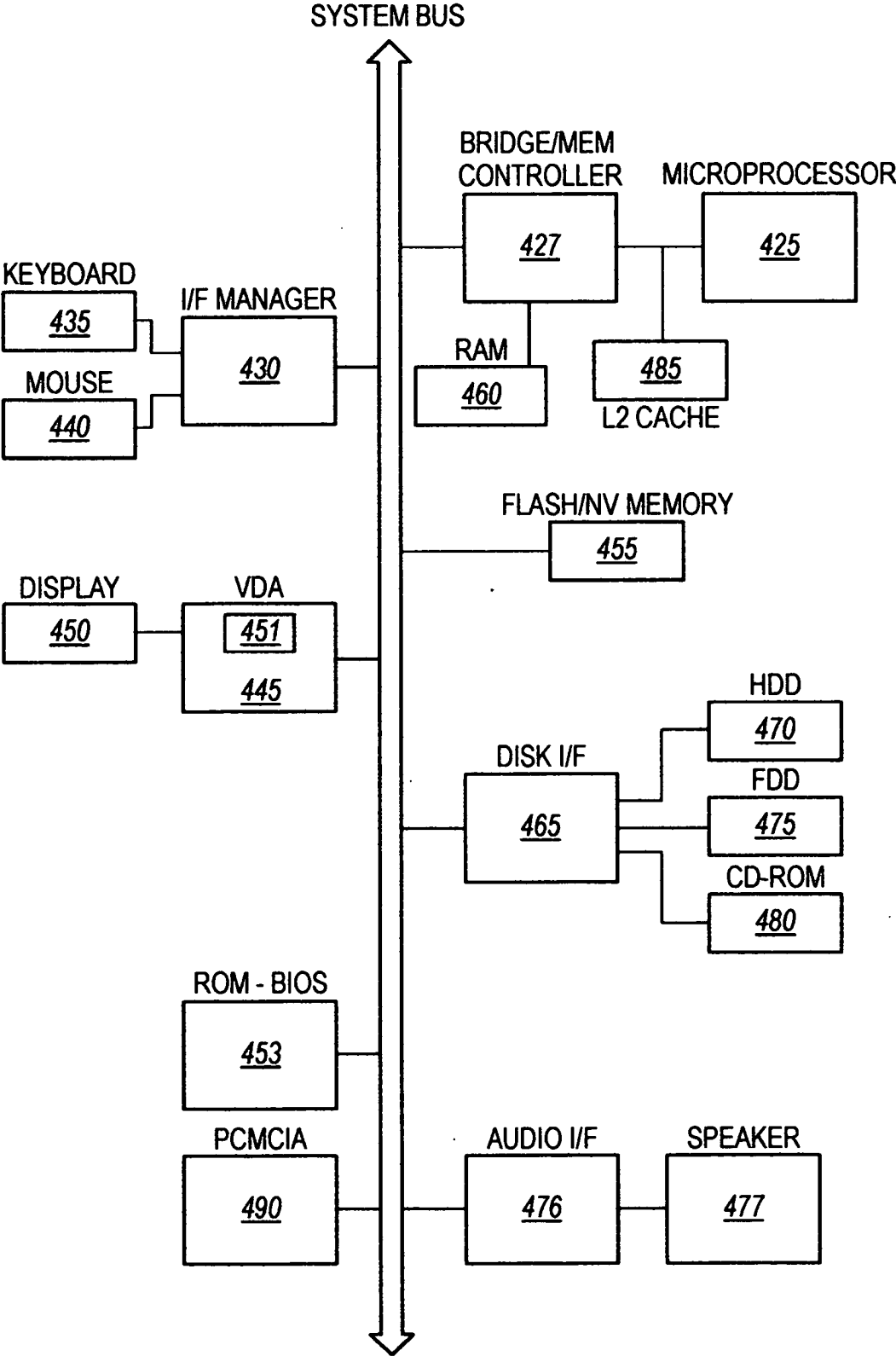
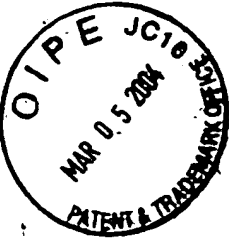


FIG. 1



TD - 155

2/14

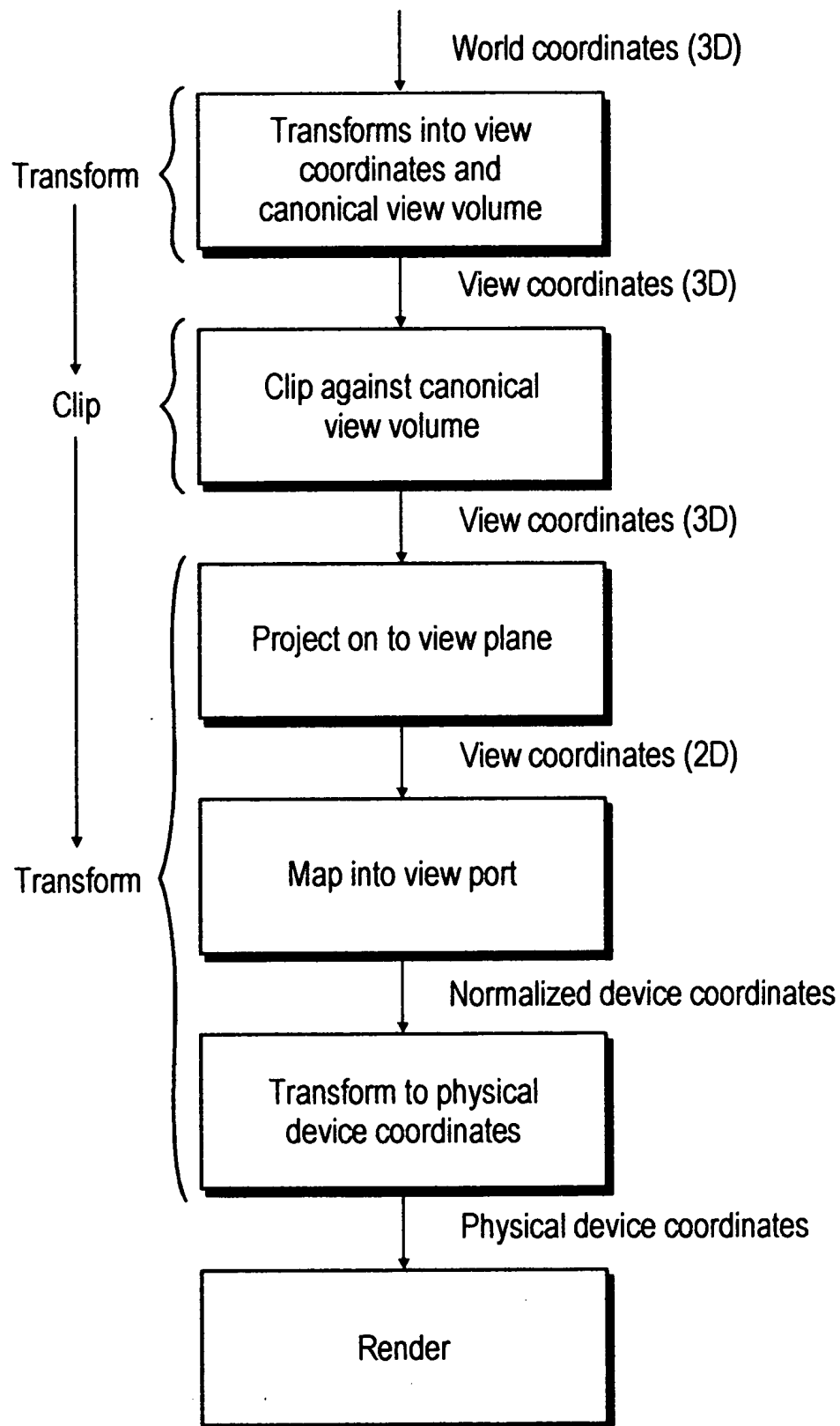


FIG. 2



TD - 155

3/14

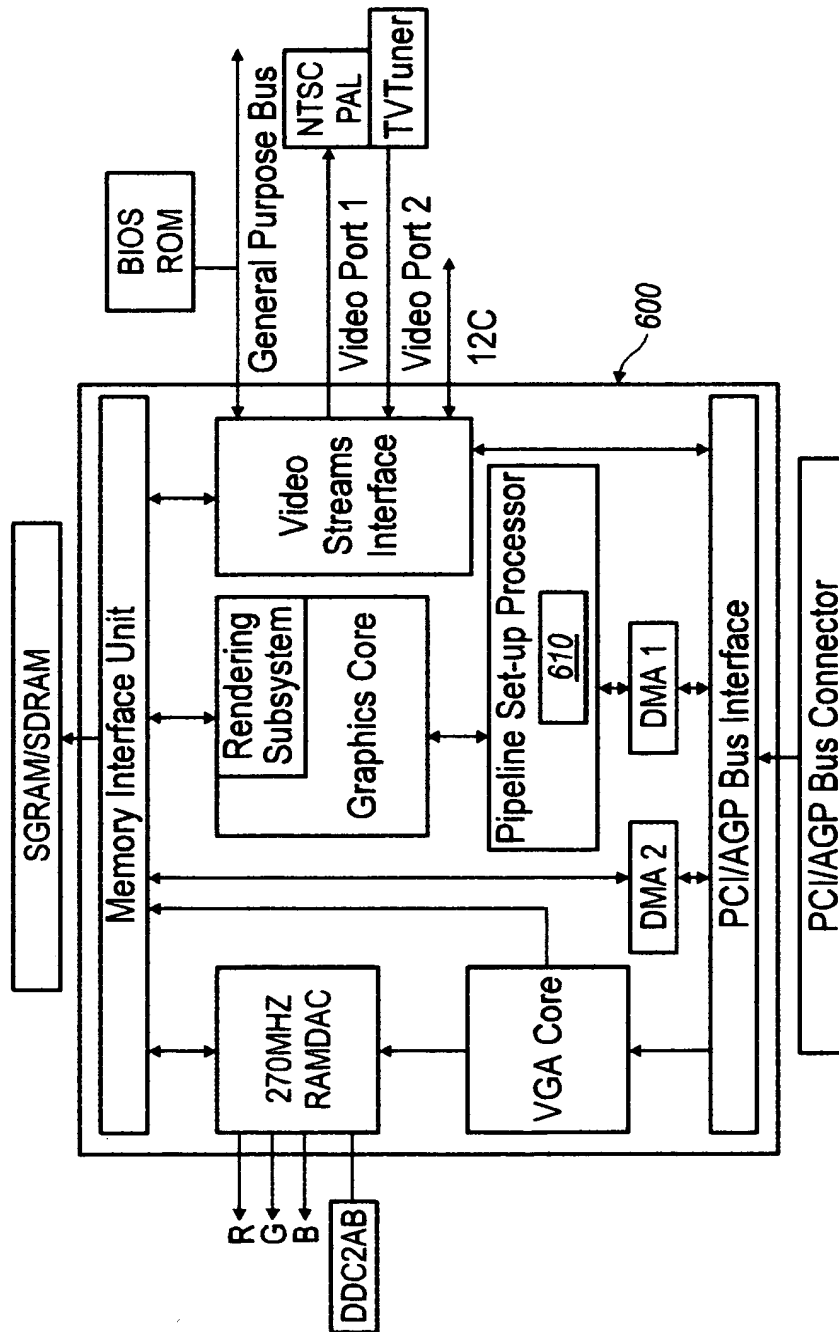
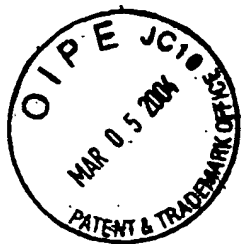


FIG. 3



TD - 155

4/14

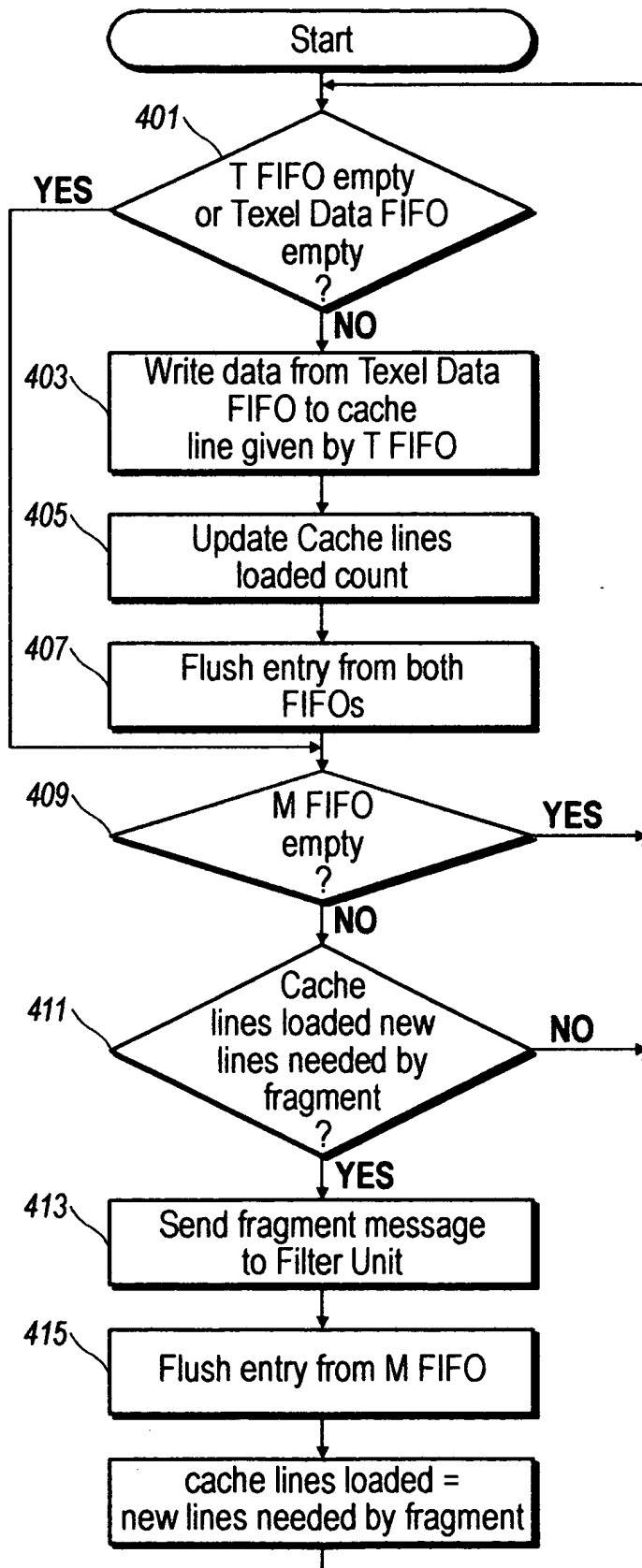
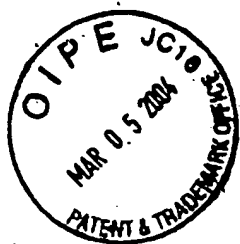


FIG. 4A



TD - 155

5/14

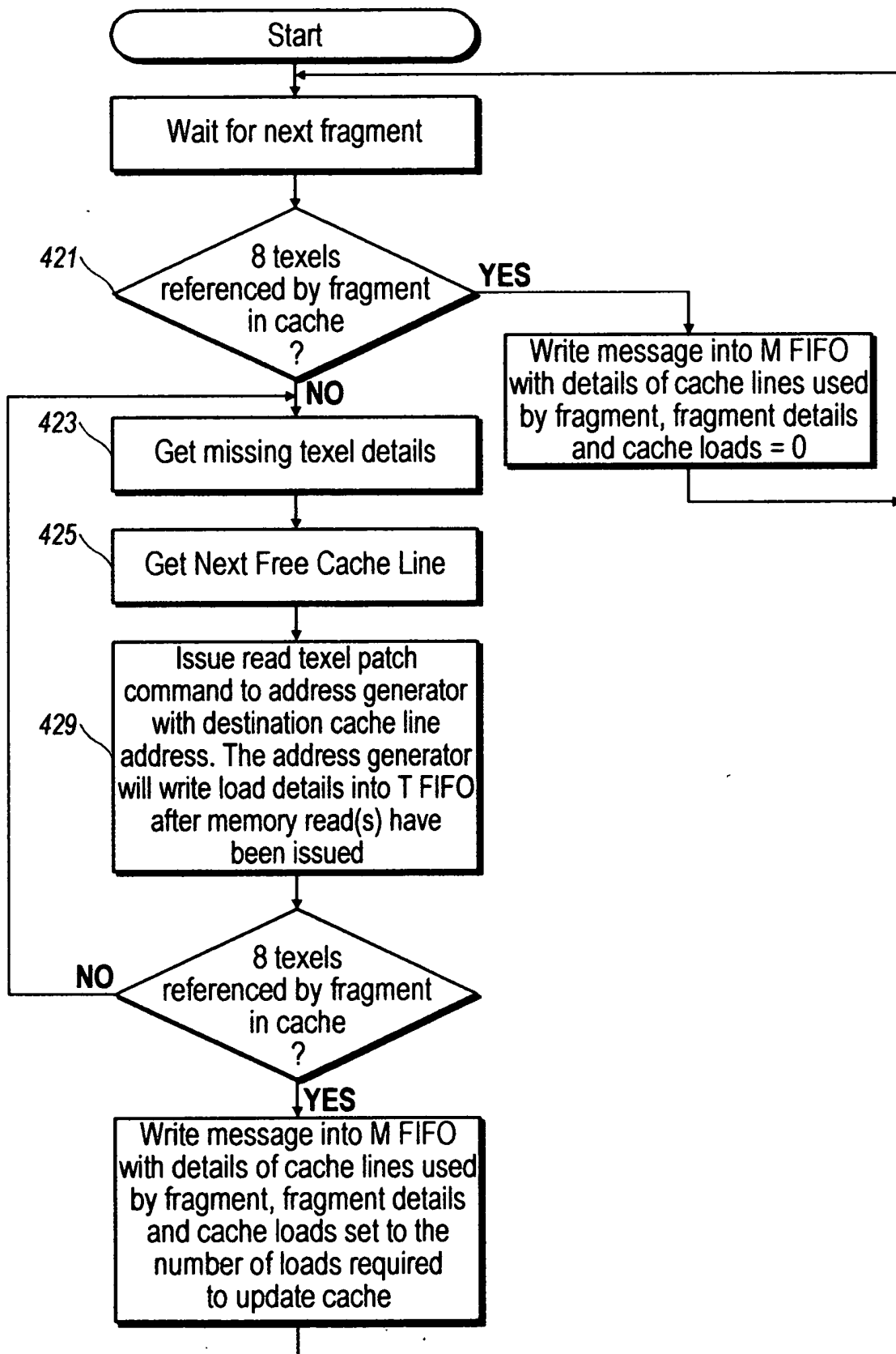
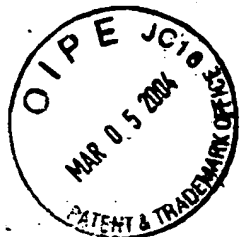


FIG. 4B



TD - 155

6/14

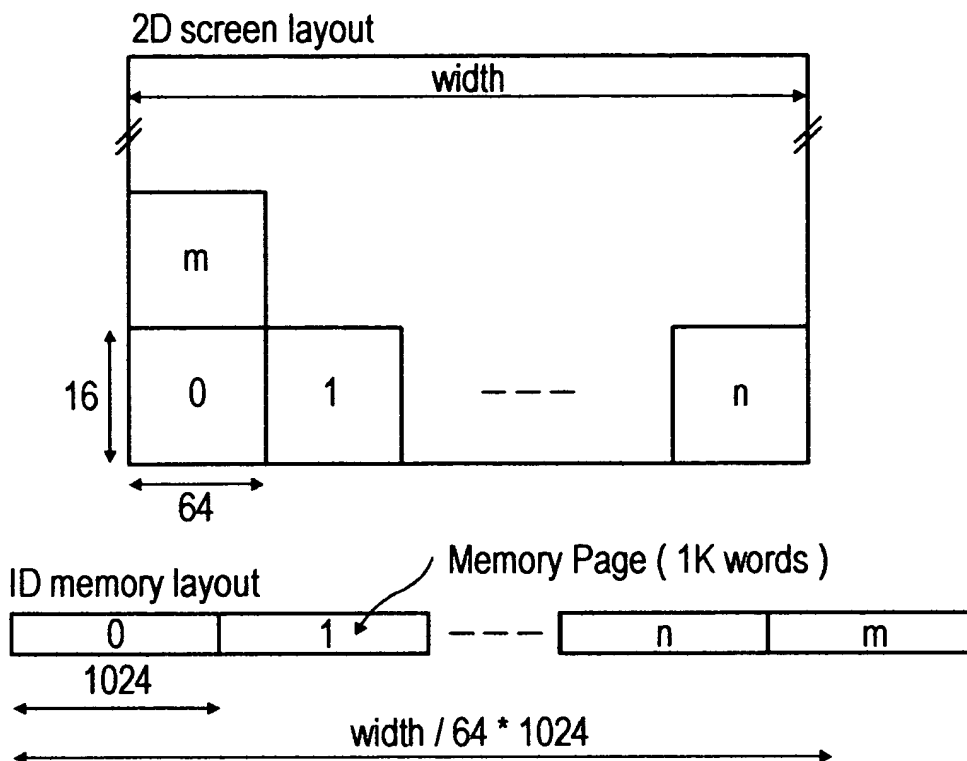
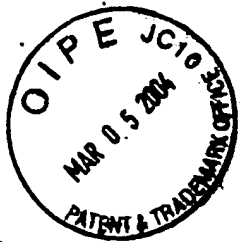


FIG. 5

T0 (0,4)	T1 (1,4)	T0 (2,4)	T1 (3,4)	T0 (4,4)	T1 (5,4)	T0 (6,4)	T1 (7,4)	T0 (8,4)	T1 (9,4)
T2 (0,3)	T3 (1,3)	T2 (2,3)	T3 (3,3)	T2 (4,3)	T3 (5,3)	T2 (6,3)	T3 (7,3)	T2 (8,3)	T3 (9,3)
T0 (0,2)	T1 (1,2)	T0 (2,2)	T1 (3,2)	T0 (4,2)	T1 (5,2)	T0 (6,2)	T1 (7,2)	T0 (8,2)	T1 (9,2)
T2 (0,1)	T3 (1,1)	T2 (2,1)	T3 (3,1)	T2 (4,1)	T3 (5,1)	T2 (6,1)	T3 (7,1)	T2 (8,1)	T3 (9,1)
T0 (0,0)	T1 (1,0)	T0 (2,0)	T1 (3,0)	T0 (4,0)	T1 (5,0)	T0 (6,0)	T1 (7,0)	T0 (8,0)	T1 (9,0)

32 bit texels in memory word
 16 bit texels in memory word
 8 bit texels in memory word

FIG. 6



TD - 155

7/14

Linear or Patch64 Memory Layouts

32 bits per texel

120	112	104	96	88	80	72	64	56	48	40	32	24	16	8	0
(3, 0)				(2, 0)				(1, 0)				(0, 0)			

16 bits per texel

120	112	104	96	88	80	72	64	56	48	40	32	24	16	8	0
(7, 0)	(6, 0)	(5, 0)	(4, 0)	(3, 0)	(2, 0)	(1, 0)	(0, 0)								

8 bits per texel

120	112	104	96	88	80	72	64	56	48	40	32	24	16	8	0
(15, 0)	(14, 0)	(13, 0)	(12, 0)	(11, 0)	(10, 0)	(9, 0)	(8, 0)	(7, 0)	(6, 0)	(5, 0)	(4, 0)	(3, 0)	(2, 0)	(1, 0)	(0, 0)

FIG. 7A

Patch32_2 or Patch2 Memory Layouts

32 bits per texel

120	112	104	96	88	80	72	64	56	48	40	32	24	16	8	0
(1, 1)				(0, 1)				(1, 0)				(0, 0)			

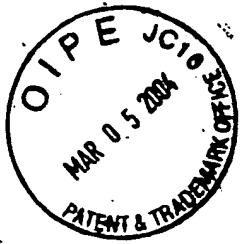
16 bits per texel

120	112	104	96	88	80	72	64	56	48	40	32	24	16	8	0
(3, 1)	(2, 1)	(3, 0)	(2, 0)	(1, 1)	(0, 1)	(1, 0)	(0, 0)								

8 bits per texel

120	112	104	96	88	80	72	64	56	48	40	32	24	16	8	0
(7, 1)	(6, 1)	(7, 0)	(6, 0)	(5, 1)	(4, 1)	(5, 0)	(4, 0)	(3, 1)	(2, 1)	(3, 0)	(2, 0)	(1, 1)	(0, 1)	(1, 0)	(0, 0)

FIG. 7B



TD - 155

8/14

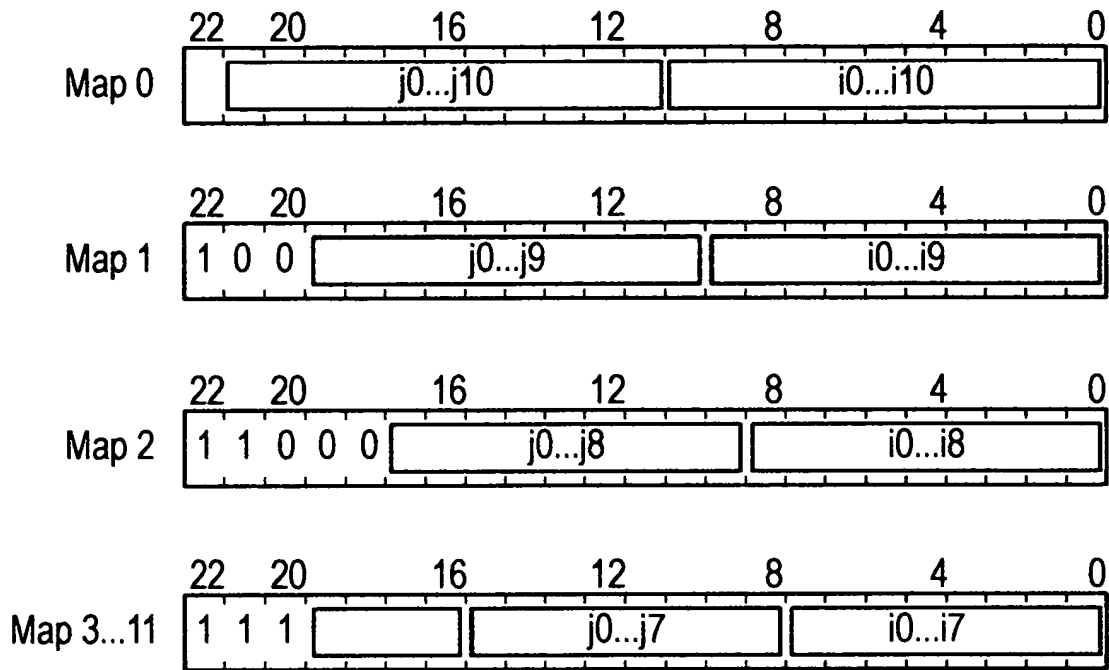


FIG. 8

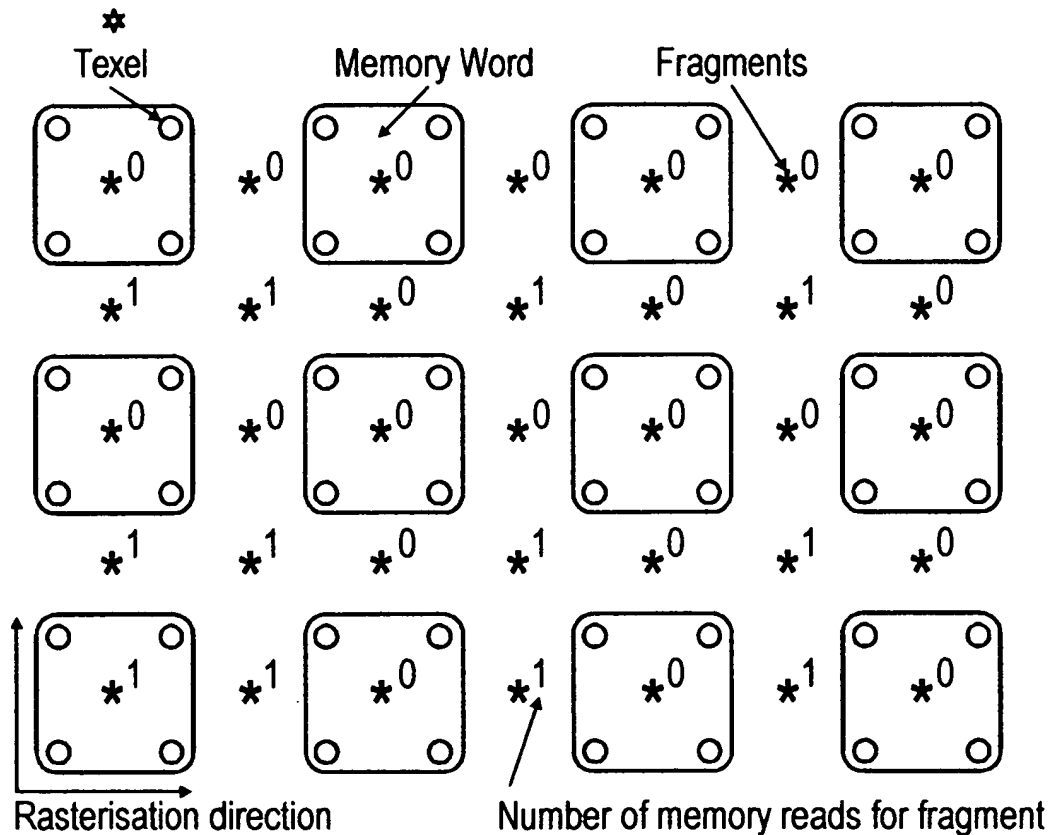


FIG. 9

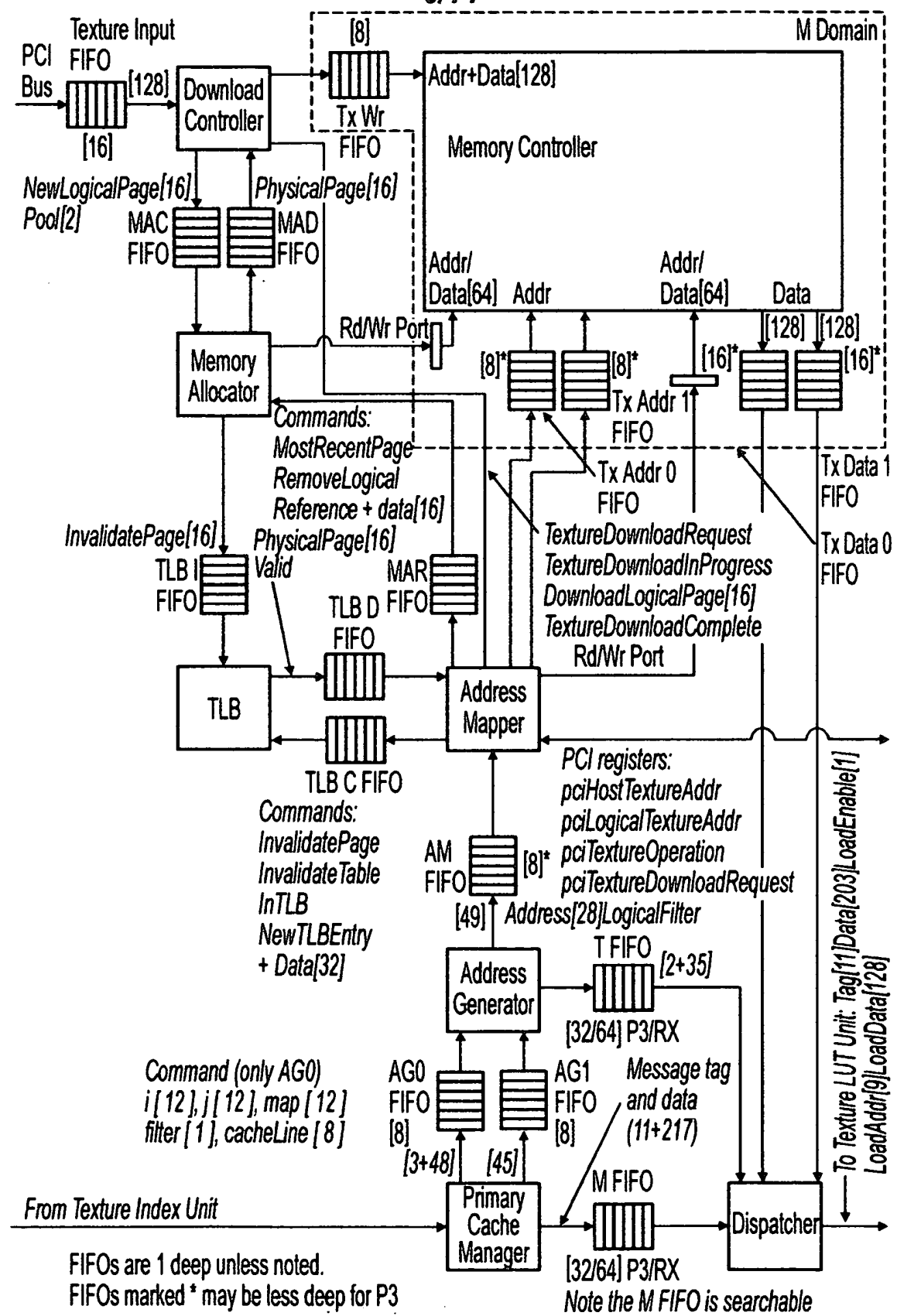
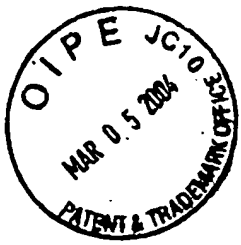


FIG. 10



TD - 155

10/14

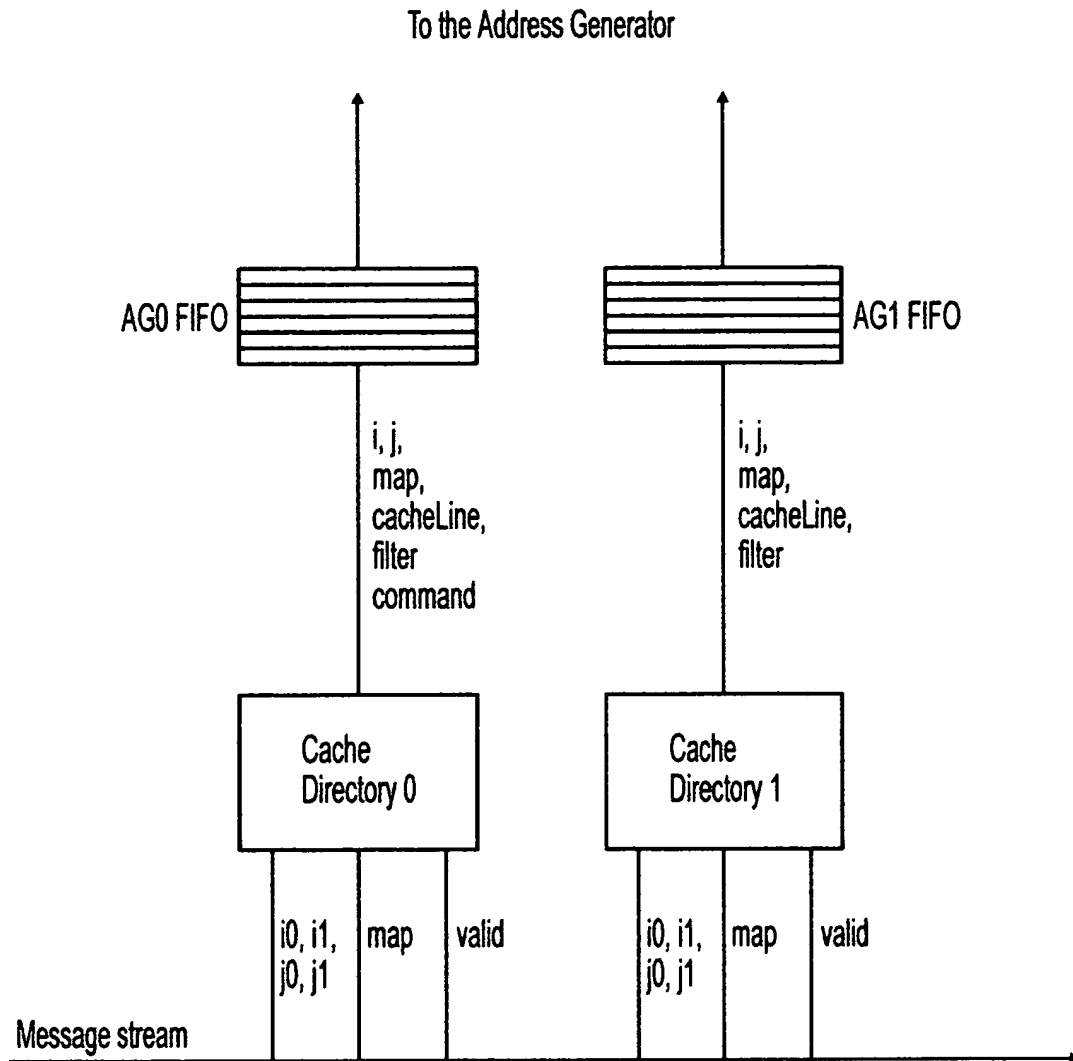
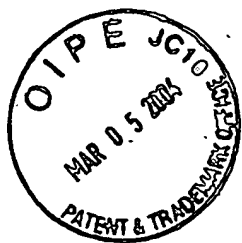


FIG. 11



TD - 155

11/14

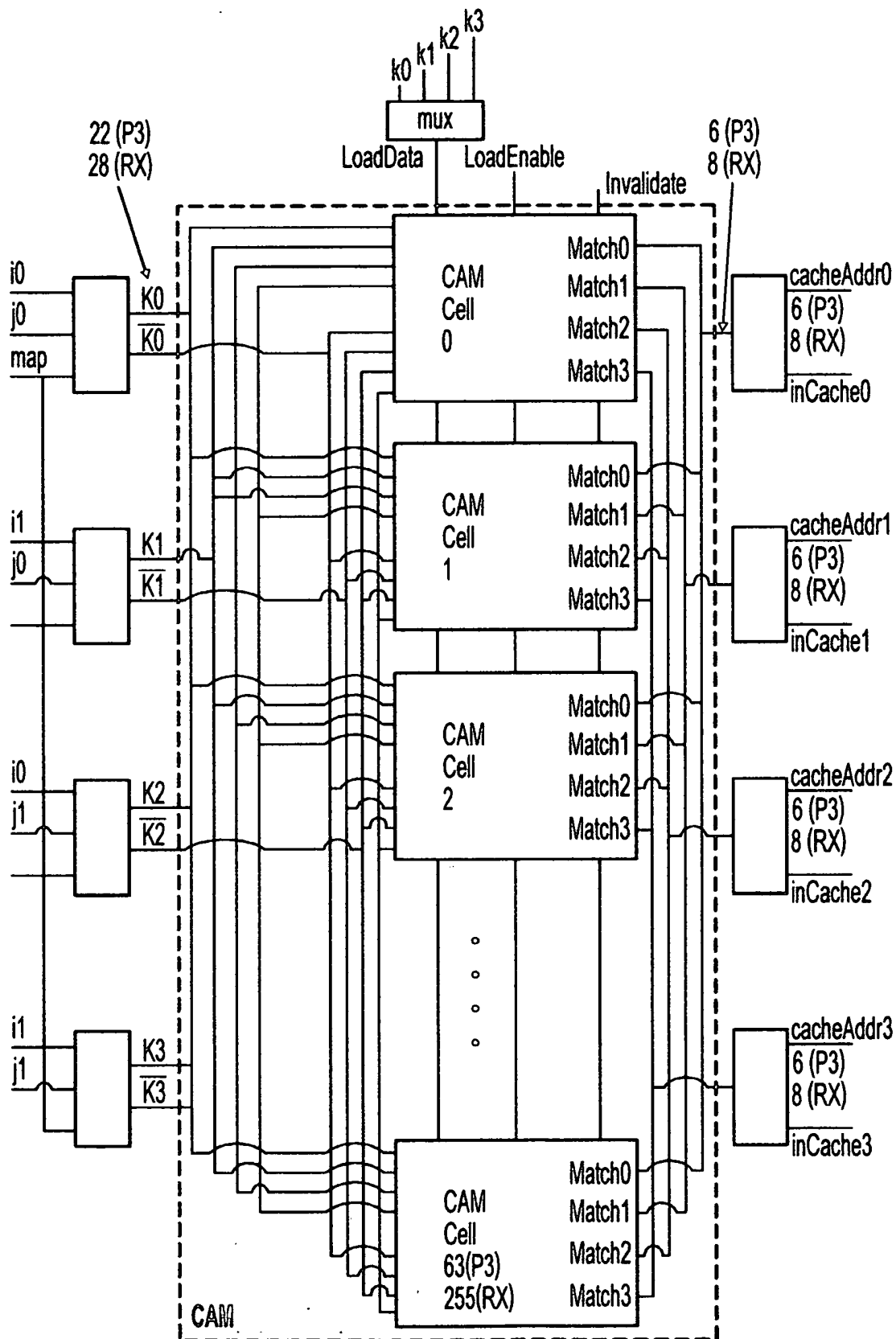


FIG. 12



TD - 155

12/14

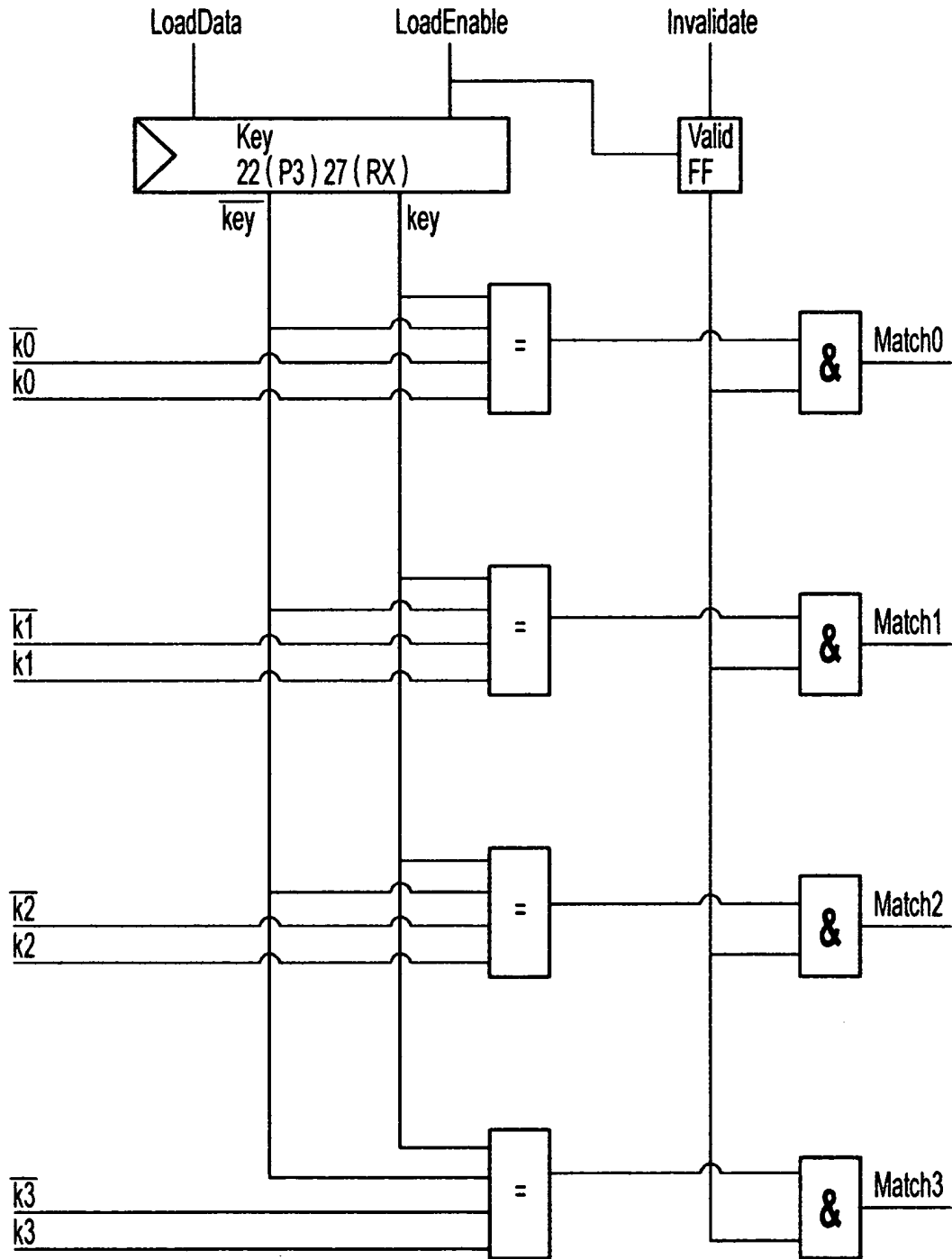


FIG. 13

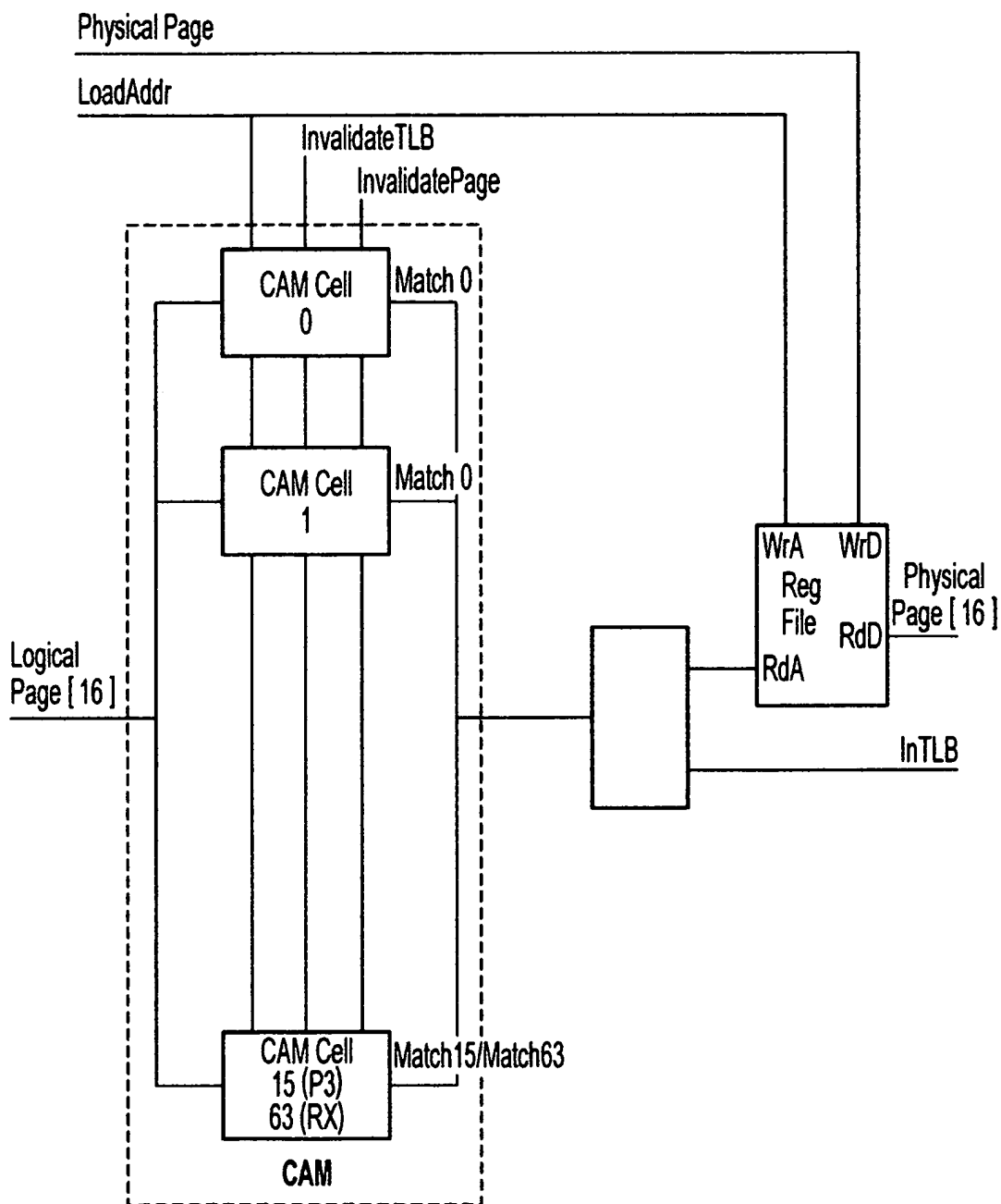
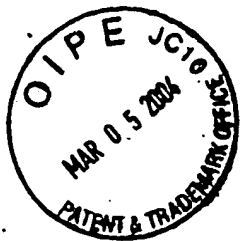
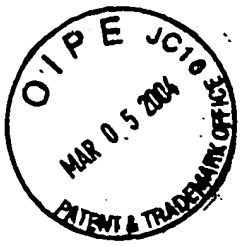


FIG. 14



TD - 155

14/14

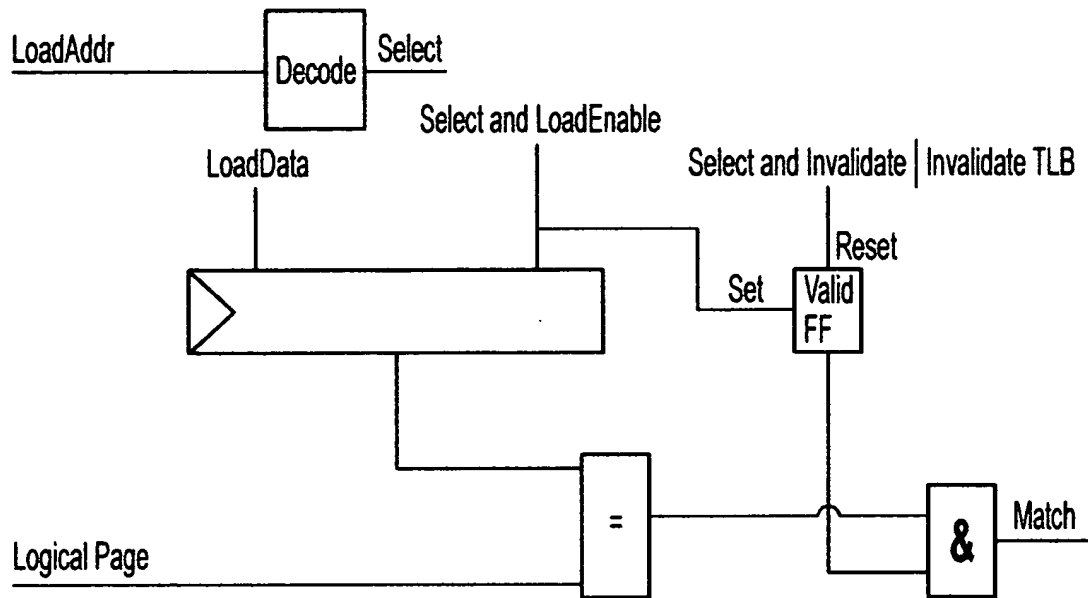


FIG. 15

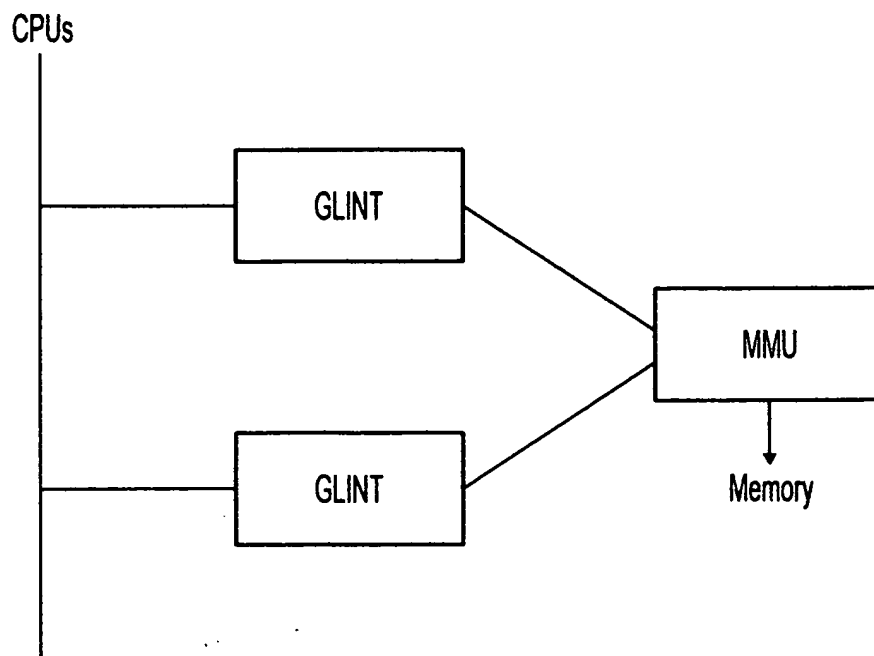


FIG. 16